

Description

The SE8403 device is a full-featured, synchronous PWM buck controller that operates at an input voltage between 9V and 100V and is optimized for external switch, maximum output 10A dc-dc converter applications.

The SE8403 has built-in protection features, such as current limit and short-circuit protection. When FB is lower than 0.35V, The device enters short-circuit protection mode, the SE8403 will repeatedly turn off for 120mS and on for 4mS again until the short-circuit condition is no longer present. The over current setting is programmable by setting one external resistor.

The output voltage of SE8403 is changed by using a resistor divider. the EN pin can disable SE8403.

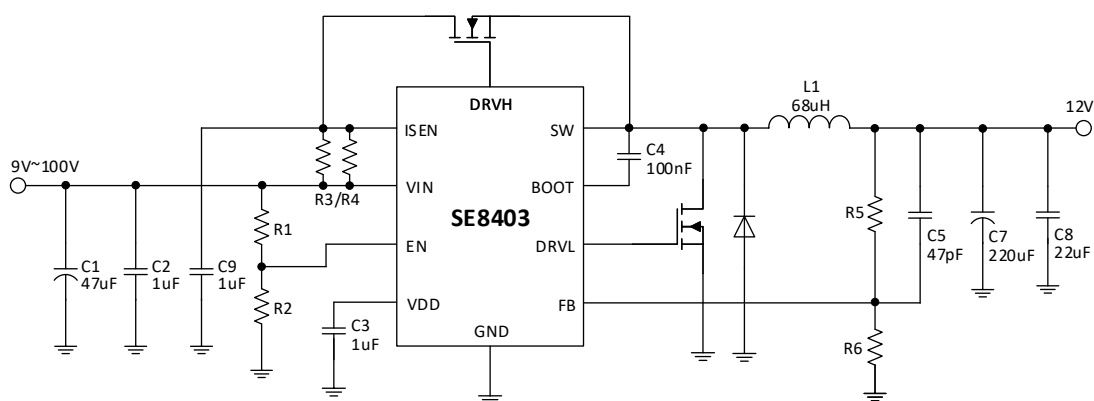
Features

- Wide Input Voltage Range: 9-100V
- Output current: 10A
- Up to 96% efficiency
- Quiescent Current 400μA
- External MOSFET
- 300kHz Fixed Frequency
- 0.8V voltage reference
- 1μA shutdown current
- 150ms Hiccup mode short circuit protection Function
- Thermal shutdown Function
- MSOP-10 package

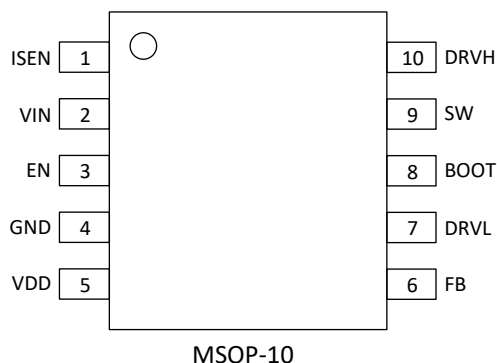
Applications

- Automotive Systems and Industry Systems
- Distributed Power Systems
- Battery Powered Systems
- GPS tracker,Charger in vehicle
- Motor Drives, Telecom

Typical Application



Pin Configuration



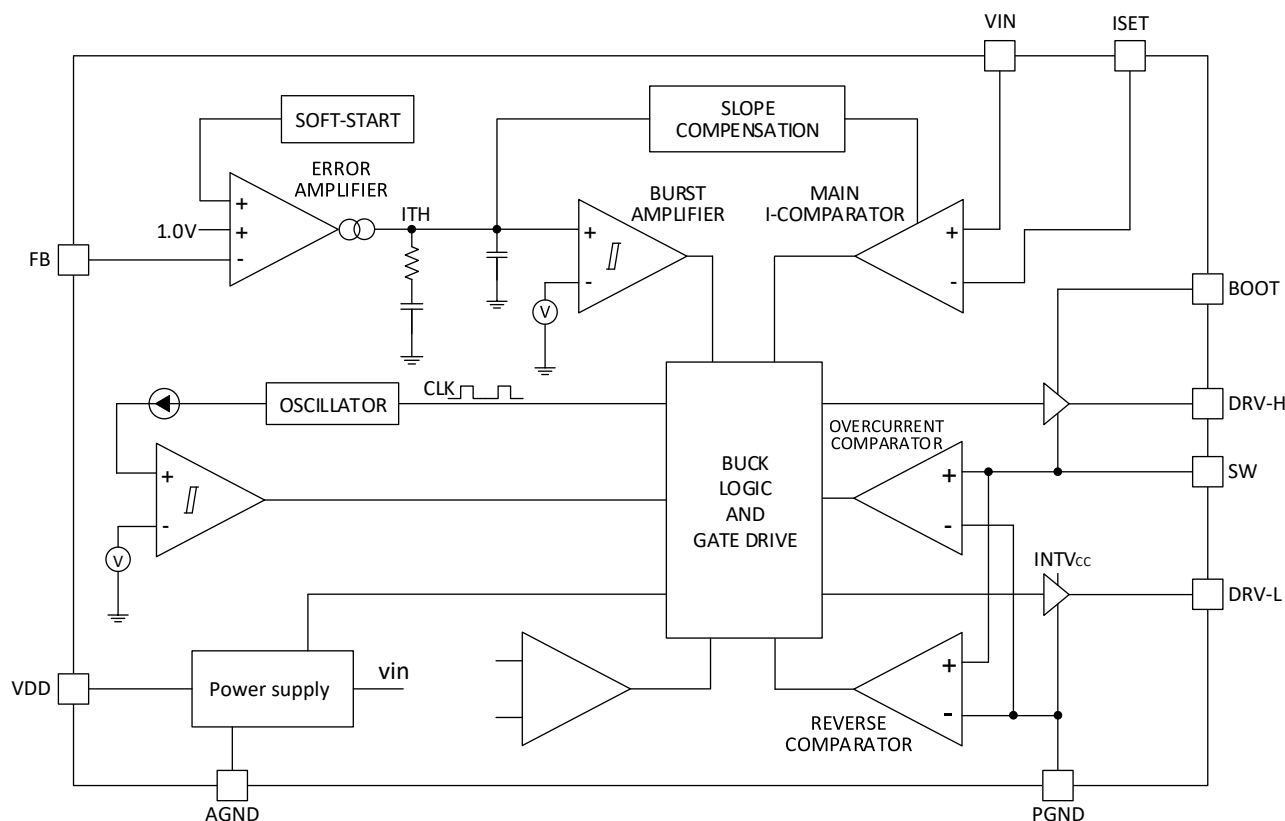
Functional Pin Description

PIN No.	Name	Description
1	ISEN	Connecting a resistance from ISEN to VIN sets the output short circuit detection threshold.
2	VIN	Input supply. VIN supplies power to all of the internal control circuitries, both BOOT regulators, and the high-side switch.
3	EN	Enable input. Pull EN below the specified threshold to shut down the SE8403. Pull EN above the specified threshold or leave EN floating to enable the SE8403.
4	GND	Ground. GND should be placed as close to the output capacitor as possible to avoid the high-current switch paths. Connect the exposed pad to GND plane for optimal thermal performance.
5	VDD	Power input to the controller.
6	FB	Feedback. FB is the input to the voltage hysteretic comparators. The average FB voltage is maintained at 800mV by loop regulation.
7	DRVL	Low Drive. Bootstrapped output for driving the gate of the low side N channel FET.
8	BOOT	Bootstrap. BOOT is the positive power supply for the internal, floating, high-side MOSFET driver. Connect a bypass capacitor between BOOT and SW.
9	SW	Switch node. SW is the output from the high-side switch. A low forward voltage Schottky rectifier to ground is required. The rectifier must be placed close to SW to reduce switching spikes.
10	DRVH	High Drive. Bootstrapped output for driving the gate of the high side N channel FET.

Ordering Information

Product model	Packaging form	Smallest packaging	Logo
SE8403	MSOP10	4000PCS	SE8403

Block Diagram



Absolute Maximum Ratings

Item	Description	Range	Unit
V_{SW} , V_{EN} , V_{IN} , V_{ISET}	SW , EN , VIN Voltage	-0.3 ~ +105	V
V_{FB} , V_{VDD}	FB , V_{VDD} Voltage	-0.3 ~ +7	V
V_{BOOT}	BOOT Voltage	$V_{SW}-0.3 \sim V_{SW}+7$	V
T_{stg}	Storage Junction Temperature	-55 ~ 150	°C
T_{solder}	Lead Temperature (Soldering 10 sec.)	260	°C
ESD	Human Body Model	2	kV

Note: exceeding the range specified by the rated parameters will cause damage to the chip, and the working state of the chip beyond the range of rated parameters cannot be guaranteed. Exposure outside the rated parameter range will affect the reliability of the chip.

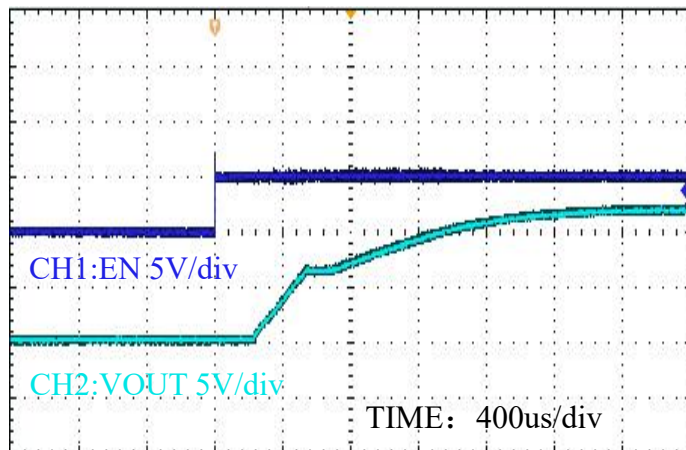
Electrical Characteristics

(At $T_A=25^{\circ}\text{C}$, $V_{IN}=48\text{V}$, $V_{OUT}=12\text{V}$, Unless Otherwise Noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
VCC SUPPLY VOLTAGE						
Input Voltage	V_{IN}		9	-	100	V
UVLO	V_{STRAT}		-	8	-	V
UVLO Hysteresis	V_{UVLO1}		-	0.3	-	V
Shutdown supply current	I_{SHUT}	$EN=0\text{V}$	-	9	-	μA
Input Quiescent Current	I_Q	$V_{FB}=1\text{V}$	-	500	-	μA
ENABLE						
Enable threshold voltage	V_{EN}		-	2.2	-	V
Enable threshold voltage Hysteresis	V_{UVLO2}		-	0.2	-	V
FEEDBACK						
FB Reference Threshold	V_{FB}		-	0.8	-	V
Feedback short voltage	$V_{FB(short)}$		-	0.35	-	V
Feedback short voltage Hysteresis	V_{FB2}		-	0.42	-	V
OSCILLATOR						
Switching frequency	F	$I_{OUT}=500\text{mA}$	-	300	-	kHz
Maximum Duty Cycle	D_{MAX}	$V_{IN}=12\text{V}$	-	92	-	%
CURRENT LIMIT						
Current Limit Threshold	I_{PEAK}	SE8403	-	-	20	A
THERMAL SHUTDOWN						
Thermal shutdown Temp	T_{SD}		-	150	-	$^{\circ}\text{C}$
Thermal shutdown Temp Hysteresis	T_{SH}		-	30	-	$^{\circ}\text{C}$

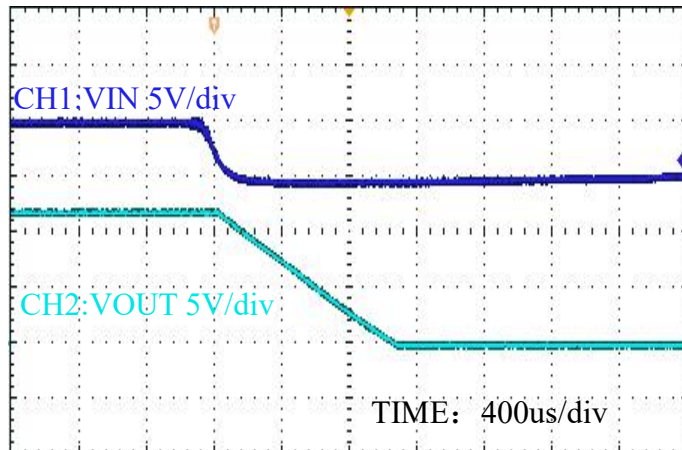
Typical Characteristics

(At $T_A=25^{\circ}\text{C}$, $V_{IN}=48\text{V}$, $V_{OUT}=12\text{V}$, Unless Otherwise Noted)



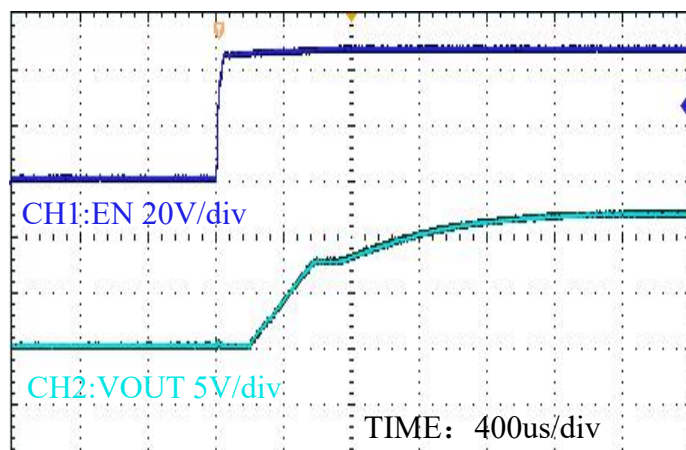
$V_{IN}=48\text{V}$ $EN=5\text{V}$

Figure1 EN Start up



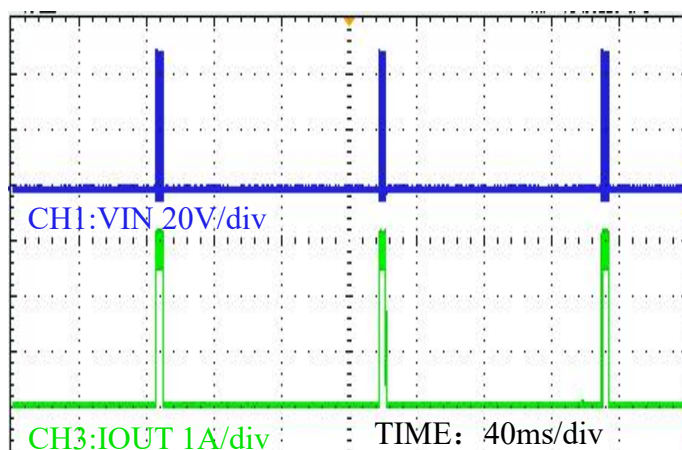
$V_{IN}=48\text{V}$ $EN=5\text{V}$

Figure2 EN Turn off



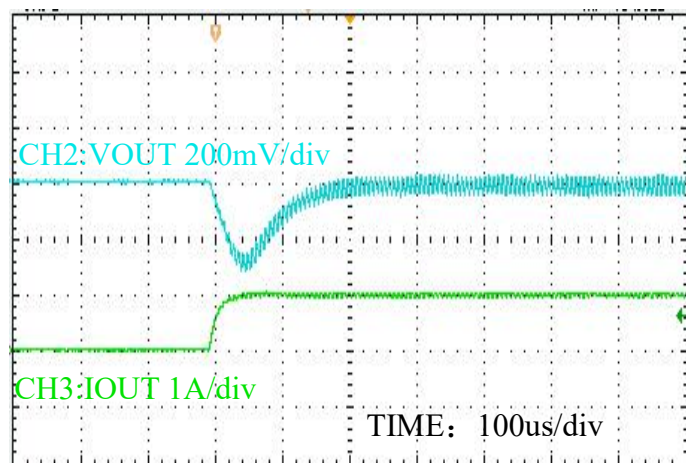
$V_{IN}=48\text{V}$ $I_{OUT}=0\text{A}$

Figure3 Start up



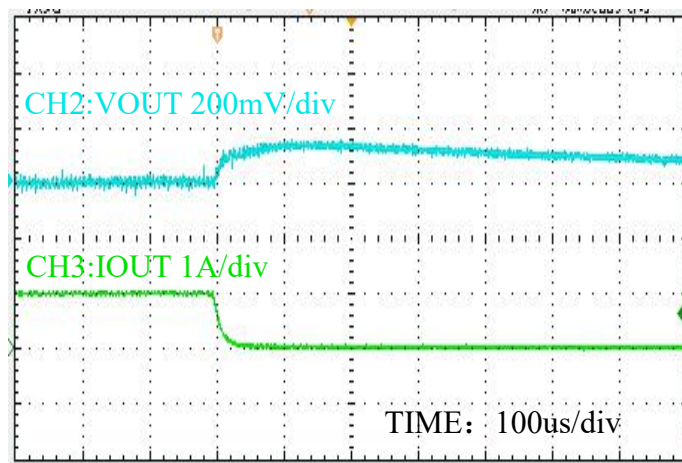
$V_{IN}=48\text{V}$ $I_{OUT}=1\text{A}$

Figure4 Start up



$I_{OUT}=10\text{mA}\sim 1\text{A}$ $V_{IN}=48\text{V}$

Figure5 Load Transient



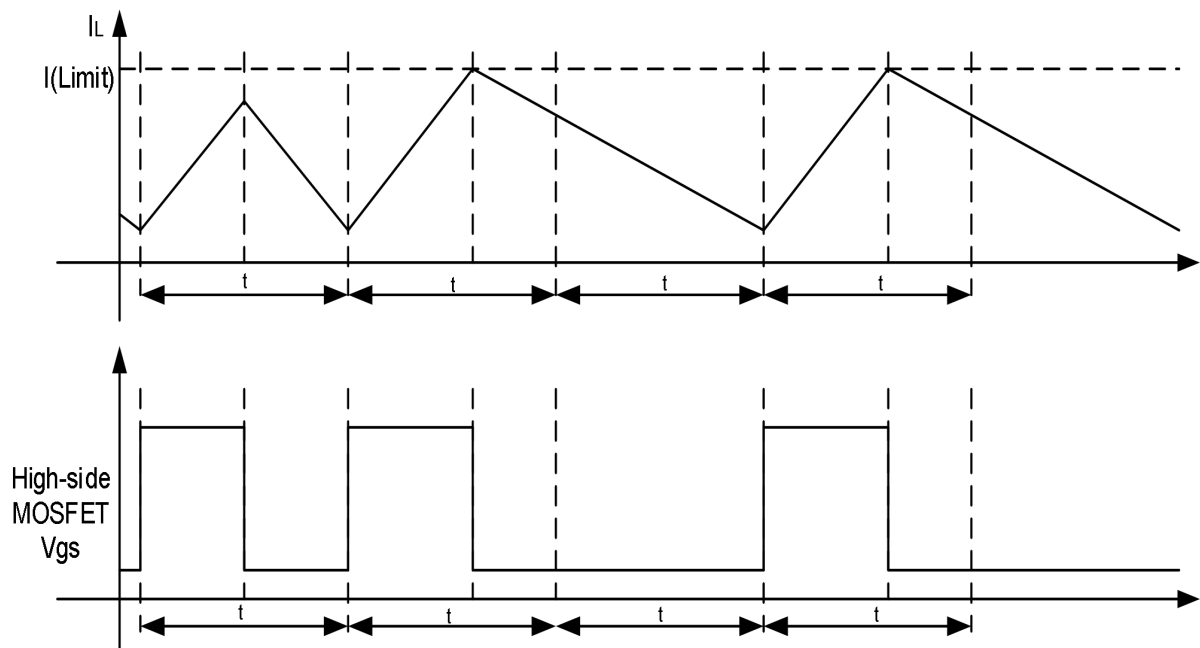
$I_{OUT}=1\text{A}\sim 10\text{mA}$ $V_{IN}=48\text{V}$

Figure6 Load Transient

Applications Information

Overcurrent Protection

The SE8403 implements current-mode control which uses the internal COMP voltage to control the turn on and the turnoff of the high-side MOSFET on a cycle-by-cycle basis. During each cycle, the switch current and the current reference generated by the internal COMP voltage are compared. When the peak switch current intersects the current reference the high-side switch turns off. Furthermore, if an output overload condition occurs for more than the hiccup wait time, which is programmed for 512 switching cycles, the device shuts down and restarts after the hiccup time of 16384 cycles. The hiccup mode helps to reduce the device power dissipation under severe overcurrent conditions.

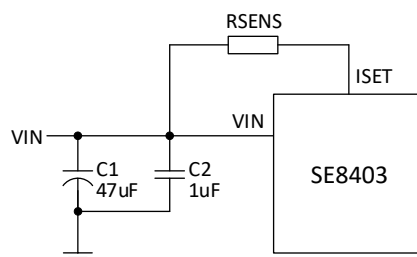


C1

This capacitor's purpose is to supply most of the switch current during the on-time, and limit the voltage ripple at VIN. To allow for the capacitor's tolerance, temperature effects, and voltage effects, a 47 μF capacitor is used.

C2

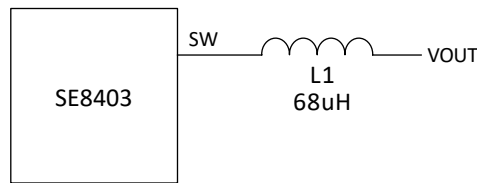
This capacitor helps avoid supply voltage transients and ringing due to long lead inductance at VIN. A low ESR, 1 μF ceramic chip capacitor is recommended, located close to the SE8403.



The capacitor on the VIN

L1

The inductance is determined based on the switching frequency, load current, inductor ripple current, and the minimum and maximum input voltages designated VIN(min) and VIN(max), respectively. The peak inductor current during an overload condition is limited to 3 A nominal. Use the value of 68 μH , 5A to prevent saturation.



The inductor on the choice

D1

A power Schottky diode is recommended. Ultra-fast recovery diodes are not recommended as the high speed transitions at the SW pin may inadvertently affect the IC's operation through external or internal EMI. The important parameters are reverse recovery time and forward voltage. The reverse recovery time determines how long the reverse current surge lasts with each turn-on of the internal buck switch. The forward voltage drop affects efficiency. The diode's reverse voltage rating must be at least as great as the maximum input voltage, plus ripple and transients, and its current rating must be at least as great as the maximum current limit specification.

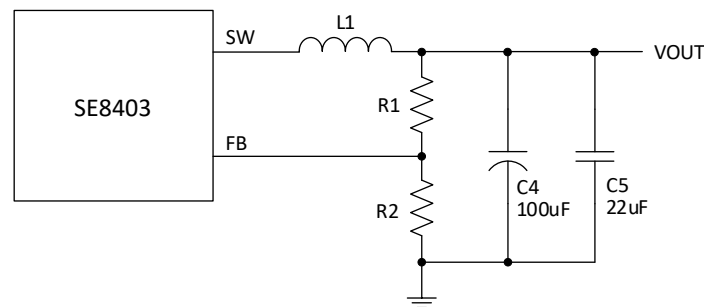
C4/C5

The output capacitor filters the inductor ripple current and provides a source of charge for transient load conditions. The best performance is typically obtained using ceramic or polymer electrolytic type components. Typical tradeoffs are that the ceramic capacitor provides extremely low ESR to reduce the output ripple voltage and noise spikes. In order to meet output ripple specification, we should choose a ceramic capacitor of 22uF and a polymer electrolytic capacitor of 100uF.

R1/R2

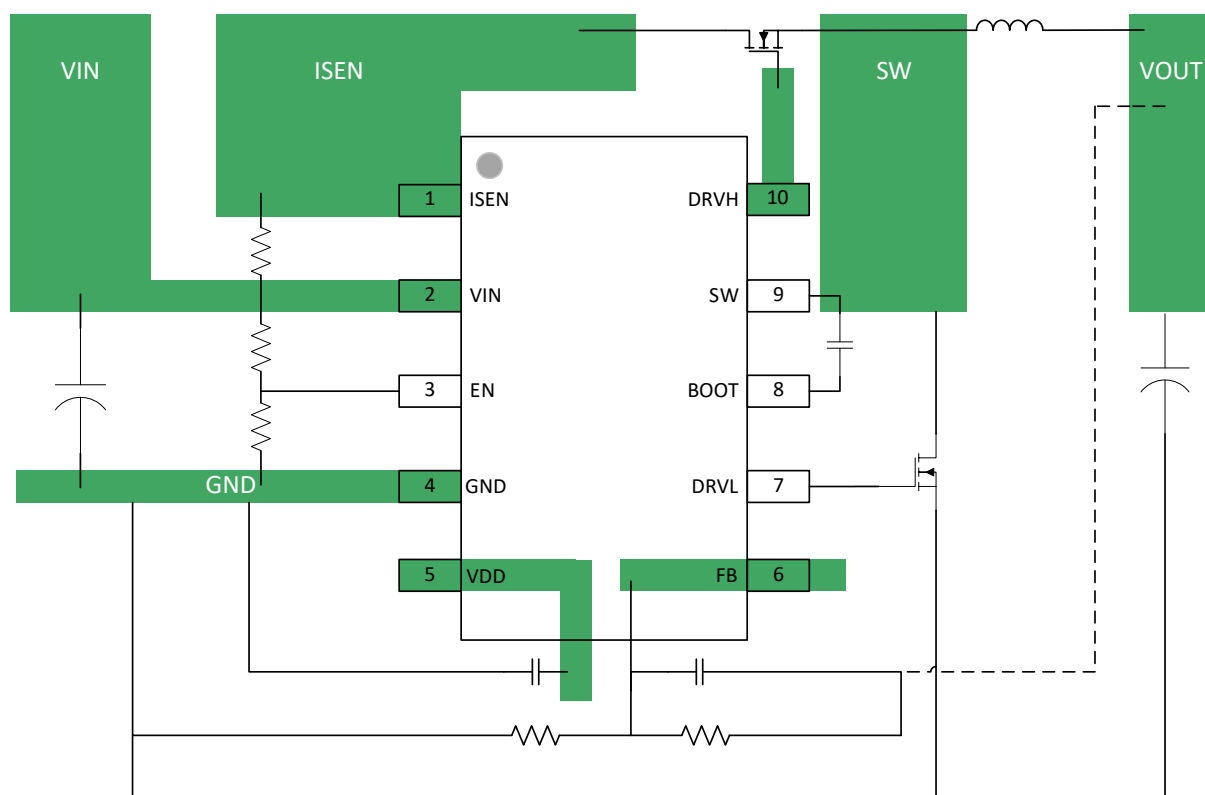
The output voltage (VOUT) is programmed by two external resistors as shown in the Figure15. The regulation point can be calculated as follows:

$$V_{OUT} = 0.8 \times (R1 + R2) / R2$$



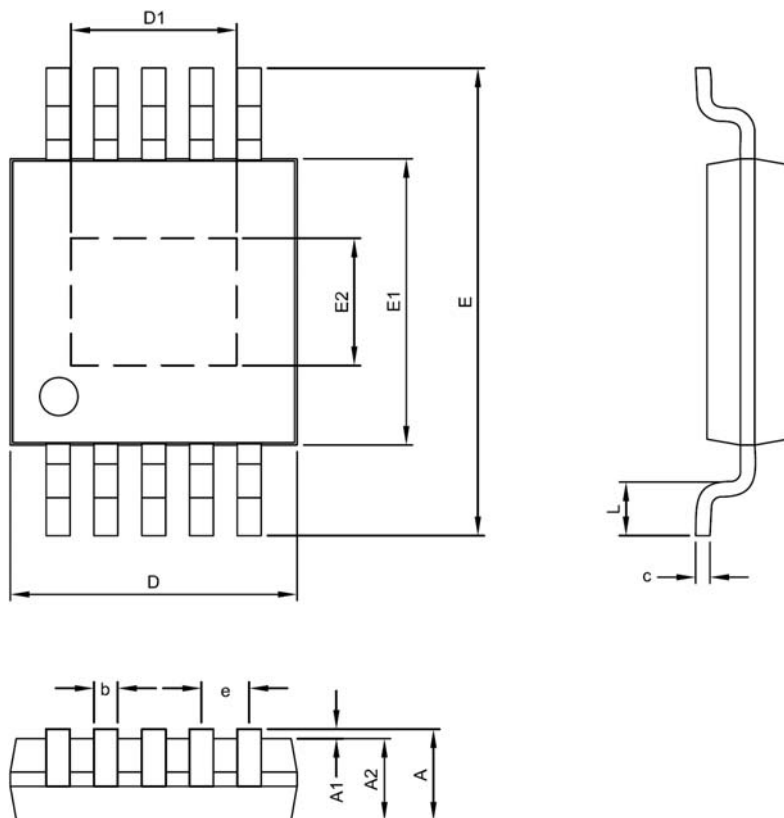
Output Capacitors and Output Configuration

Layout



Package Information

MSOP-10



Symbol	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.81	0.95	1.10	0.032	0.037	0.043
A1	0.00	---	0.15	0.000	---	0.006
A2	0.75	0.86	0.96	0.030	0.034	0.038
D	2.90	3.00	3.10	0.114	0.118	0.122
D1	2.00 REF			0.79 REF		
E	4.80	4.90	5.00	0.189	0.193	0.197
E1	2.90	3.00	3.10	0.114	0.118	0.122
E2	1.60 REF			0.063 REF		
b	0.15	0.25	0.35	0.006	0.010	0.014
c	0.15 REF			0.006 REF		
e	0.50 BSC			0.020 BSC		
L	0.40	0.53	0.80	0.016	0.021	0.031